

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Michael D. HAINES

Serial No.: 10/024,724

Filing Date: December 21, 2001

For:

METHOD AND APPARATUS FOR INCREASING THE

IMMUNITY OF NEW GENERATION MICROPROCESSORS

FROM ESD EVENTS

LETTER SUBMITTING FORMAL DRAWINGS

Assistant Commissioner for Patents Washington, D.C. 20231

February 6, 2002

Sir:

Submitted herewith are two (2) sheets of formal drawings illustrating Figs.

1-3 in connection with the above-identified application.

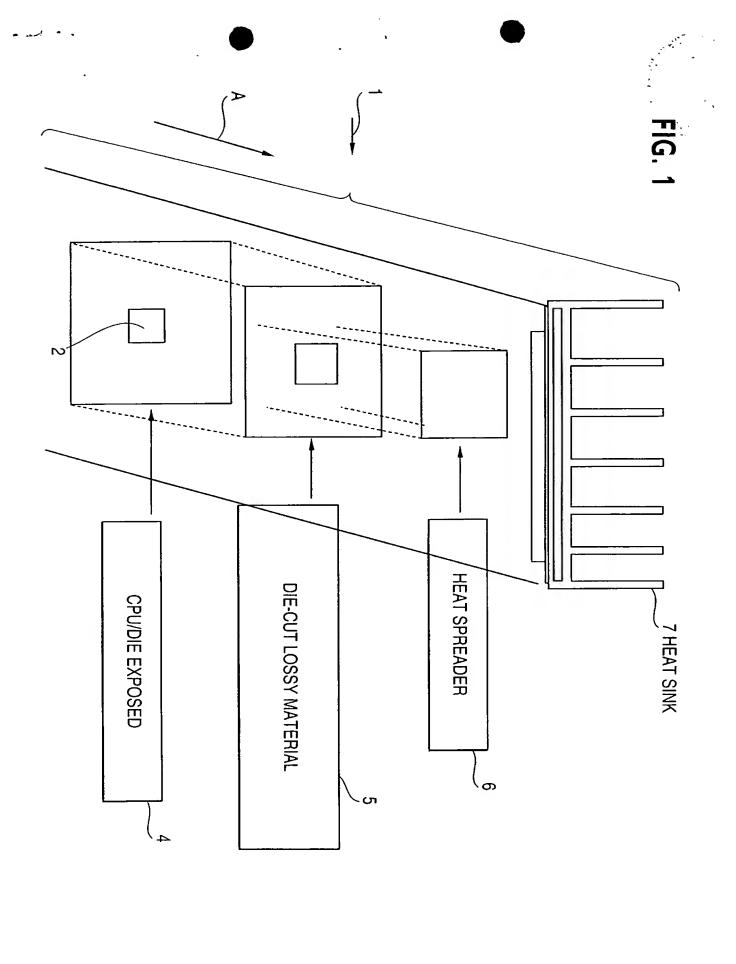
Respectfully submitted,

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APPROVEU D.G. FIG.

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